Ordering Information

Applicable standards

Refer to the OMRON website (www.ia.omron.com) or ask your OMRON representative for the most recent applicable standards for each model.

CPU Unit

CP2E-N-type/Network model

				Specific	cations				
Number of points				Output	Program	Data memory	Current co	nsumption	Model
	Power Supply	Inputs	Outputs	type	capacity	capacity	5 VDC	24 VDC	
CPU Units with				Relay			0.15 A	0.05 A	CP2E-N14DR-A
14 points	100 to 240 VAC			Transistor (sinking)			0.21 A	0.02 A	CP2E-N14DT-A
minine"		8 points	6 points	Relay			0.15 A	0.05 A	CP2E-N14DR-D
	24 VDC	o pointo	o pomio	Transistor (sinking)			0.21 A	0.02 A	CP2E-N14DT-D
				Transistor (sourcing)			0.22 A	0.02 A	CP2E-N14DT1-D
CPU Units with				Relay			0.17 A	0.06 A	CP2E-N20DR-A
20 points	100 to 240 VAC			Transistor (sinking)			0.27 A	0.02 A	CP2E-N20DT-A
minut.		12 points	8 points	Relay			0.17 A	0.06 A	CP2E-N20DR-D
E	24 VDC			Transistor (sinking)			0.27 A	0.02 A	CP2E-N20DT-D
				Transistor (sourcing)		16K words	0.26 A	0.02 A	CP2E-N20DT1-D
CPU Units with				Relay	10K steps (FB capacity: 10K steps)		0.41 A	0.07 A	CP2E-N30DR-A
30 points	100 to 240 VAC			Transistor (sinking)			0.52 A	0.03 A	CP2E-N30DT-A
- minima.		18 points	ts 12 points	Relay			0.37 A	0.07 A	CP2E-N30DR-D
	24 VDC			Transistor (sinking)			0.51 A	0.03 A	CP2E-N30DT-D
				Transistor (sourcing)			0.51 A	0.03 A	CP2E-N30DT1-D
CPU Units with				Relay			0.39 A	0.09 A	CP2E-N40DR-A
40 points	100 to 240 VAC			Transistor (sinking)			0.59 A	0.03 A	CP2E-N40DT-A
- minimina		24 points	16 points	Relay			0.39 A	0.09 A	CP2E-N40DR-D
	24 VDC			Transistor (sinking)			0.59 A	0.03 A	CP2E-N40DT-D
				Transistor (sourcing)			0.59 A	0.03 A	CP2E-N40DT1-D
CPU Units with				Relay			0.44 A	0.13 A	CP2E-N60DR-A
60 points	100 to 240 VAC			Transistor (sinking)			0.71 A	0.03 A	CP2E-N60DT-A
		36 points	24 points	Relay			0.41 A	0.13 A	CP2E-N60DR-D
E STATE OF THE STA	24 VDC	23 200	24 points	Transistor (sinking)]		0.71 A	0.03 A	CP2E-N60DT-D
				Transistor (sourcing)			0.71 A	0.03 A	CP2E-N60DT1-D

CP2E-S-type/Standard model

				Specific	cations												
Number of points	Power Supply	Inputs	Outputs	Output	Program	Data memory	Current co	nsumption	Model								
	Power Supply	iliputs	Outputs	type	capacity	capacity	5 VDC	24 VDC									
CPU Units with 30 points	100 to 240 VAC			Relay			0.12 A	0.07 A	CP2E-S30DR-A								
- manual of	24.)/DC	18 points	12 points	12 points Transistor (sinking)				0.00.4	CP2E-S30DT-D								
	24 VDC			Transistor (sourcing)		CP2E-S30DT1-D											
CPU Units with 40 points	100 to 240 VAC			Relay			0.13 A	0.09 A	CP2E-S40DR-A								
- manana	24 VDC	24 points	16 points	Transistor (sinking)	8K steps (FB capacity:	8K words	0.34 A 0.02 A	0.02.4	CP2E-S40DT-D								
	24 VDG			Transistor (sourcing)	8K steps)		0.54 A	0.02 A	CP2E-S40DT1-D								
CPU Units with 60 points	100 to 240 VAC			Relay	Relay	Relay	Relay	Relay	Relay	Relay	Relay	Relay			0.16 A	0.13 A	CP2E-S60DR-A
annimum min	24.VDC	36 points	24 points	Transistor (sinking)				0.02.4	CP2E-S60DT-D								
	24 VDC			Transistor (sourcing)			0.48 A	0.02 A	CP2E-S60DT1-D								

CP2E-E-type/Essential model

				Specifi	cations				
Number of points	Power Supply	Inputs	Outputs	Output	Program	Data memory	Current consumption		Model
	Power Supply	iliputs	Outputs	type	capacity	capacity	5 VDC 24 VDC		
CPU Units with 14 points		8 points	6 points	Relay			0.06 A	0.04 A	CP2E-E14DR-A
CPU Units with 20 points		12 points	8 points	Relay			0.08 A	0.06 A	CP2E-E20DR-A
CPU Units with 30 points	100 to 240 VAC	18 points	12 points	Relay	4K steps (FB capacity: 4K steps)	4K words	0.12 A	0.07 A	CP2E-E30DR-A
CPU Units with 40 points		24 points	16 points	Relay			0.13 A	0.09 A	CP2E-E40DR-A
CPU Units with 60 points		36 points	24 points	Relay			0.16 A	0.13 A	CP2E-E60DR-A

Optional Products

Battery

Product name	Specifications	Model
Battery		
	Mounted in an N/S□□-type CPU Unit. Mount the Battery when using the clock function. A Battery cannot be mounted to an E□□-type CPU Unit.	CP2W-BAT02

Option Board

Product name	Specifications		rrent mption	Model	
		5 VDC	24 VDC		
RS-232C Option Board	Mounted in the option slot of an N□□-type CPU Unit and can be used as an RS-232C port. Maximum transmission distance: 15 m	0.04 A		CP1W-CIF01	
Jon-isolated RS-422A/485 Option Board	d Mounted in the option slot of an N□□-type CPU Unit and can be used as an RS-422A/485 port. Maximum transmission distance: 50 m	0.04 A		CP1W-CIF11	
solated RS-422A/485 Option Board	Mounted in the option slot of an N□□-type CPU Unit and can be used as an RS-422A/485 port. Maximum transmission distance: 500 m	0.04 A		CP1W-CIF12-V1	
RS-232C&RS-232C Option Board	Mounted in the option slot of an N□□-type CPU Unit and can be used as two RS-232C ports. Maximum transmission distance: 15 m	0.04 A		CP2W-CIFD1	
RS-232C&RS-485 Option Board	Mounted in the option slot of an N□□-type CPU Unit and can be used as one RS-232C port and one isolated RS-485 port. Maximum transmission distance: 15 m (RS-232C) 500 m (RS-485)	0.06 A		CP2W-CIFD2	
RS-485&RS-485 Option Board	Mounted in the option slot of an N□□-type CPU Unit and can be used as two isolated RS-485 ports. Maximum transmission distance: 500 m	0.08 A		CP2W-CIFD3	
nalog Input Option Board	Mounted in the option slot of an N□□-type CPU Unit and can be used as an analog input module. • 2 analog inputs 0 to 10 V (Resolution: 1/4000) 0 to 20 mA (Resolution: 1/2000)	0.02 A		CP1W-ADB21	
nalog Output Option Board	Mounted in the option slot of an N□□-type CPU Unit and can be used as an analog output module. • 2 analog outputs 0 to 10 V (Resolution: 1/4000)	0.06 A		CP1W-DAB21V	
Analog Input/Output Option Board	Mounted in the option slot of an N□□-type CPU Unit and can be used as an analog input/output module. • 2 analog inputs 0 to 10 V (Resolution: 1/4000) 0 to 20 mA (Resolution: 1/2000) • 2 analog outputs 0 to 10 V (Resolution: 1/4000)	0.08 A		CP1W-MAB221	

- Note: 1. Maximum one Analog Option Board can be mounted on an N□□-type CPU Unit.

 2. The CP1W-ME05M Memory Cassette, CP1W-DAM01 LCD Option Board, and CP1W-CIF41 Ethernet Option Board cannot be used with the CP2E CPU Unit.
 - 3. Option Boards cannot be used with the E/S \Box -type CPU Unit.

Expansion I/O Units and Expansion Units (for E30/40/60, S30/40/60, or N30/40/60 CPU Units) E14/20 or N14/20 CPU Units do not support Expansion I/O Units and Expansion Units.

Unit type	Product name			Specifications			rent ption (A)	Model
		Inputs	Inputs Outputs Output type			5 V	24 V	
	Input Unit	8		24 VDC Input		0.018		CP1W-8ED
	Output Units			Relay	0.026	0.044	CP1W-8ER	
			8	Transistor (sinking)		0.075		CP1W-8ET
				Transistor (sourcing)		0.075		CP1W-8ET1
	o			Relay		0.042	0.090	CP1W-16ER
			16	Transistor (sinking)		0.076		CP1W-16ET
P1W Expansion Of Units	FARMENDER			Transistor (sourcing)		0.076		CP1W-16ET1
· Omio	Amana Cara			Relay		0.049	0.131	CP1W-32ER
			32	Transistor (sinking)		0.113		CP1W-32ET
	· Paramanan			Transistor (sourcing)		0.113		CP1W-32ET1
	I/O Units			Relay		0.103	0.044	CP1W-20EDR1
	o	12	8	Transistor (sinking)		0.130		CP1W-20EDT
in the second of	TRANSPORTER OF	12		Transistor (sourcing)		0.130		CP1W-20EDT1
			Relay		0.080	0.090	CP1W-40EDR	
		24	16	Transistor (sinking)		0.160		CP1W-40EDT
	- Patriciani			Transistor (sourcing)		0.160		CP1W-40EDT1
Ana	Analog Input Unit	4CH		Input range: 0 to 5 V, 1 to 5 V, 0 to 10 V, ±10 V,	Resolution: 1/6000	0.100	0.090	CP1W-AD041
	in the state of th		4CH	0 to 20 mA, or 4 to 20 mA.	Resolution: 1/12000	0.100	0.050	CP1W-AD042
	Analog Output Unit		2CH		Resolution: 1/6000	0.040	0.095	CP1W-DA021
			4CH	Output range: 1 to 5 V, 0 to 10 V, ±10 V, 0 to 20 mA, or 4 to 20 mA.	Resolution: 1/6000	0.080	0.124	CP1W-DA041
	(Marion)		1011	·	Resolution: 1/12000	0.070	0.160	CP1W-DA042
	Analog I/O Unit	4CH	4CH	Input range: 0 to 5 V, 1 to 5 V, 0 to 10 V, ±10 V,	Resolution: 1/12000	0.120	0.170	CP1W-MAD44
P1W Expansion		4CH	2CH	0 to 20 mA, or 4 to 20 mA. Output range: 1 to 5 V, 0 to 10 V, ±10 V,	Resolution: 1/12000	0.120	0.120	CP1W-MAD42
nits	FREE FREE N	2CH	1CH	0 to 20 mA, or 4 to 20 mA.	Resolution: 1/6000	0.083	0.110	CP1W-MAD11
	Temperature Sensor Unit	2CH		Sensor type: Thermocouple		0.040	0.059	CP1W-TS001
	ō	4CH		Sensor type: Thermocouple	,	0.040	0.059	CP1W-TS002
		2CH		Sensor type: Platinum resistance thermometer (Pt100 or JPt100)		0.054	0.073	CP1W-TS101
		4CH		Sensor type: Platinum resista thermometer (Pt100 or JPt10		0.054	0.073	CP1W-TS102
		4CH		Sensor type: Thermocouple (J or K) 2channels can be used as analog input. Input range: 1 to 5 V, 0 to 10 V, 4-20 mA	Resolution: 1/12000	0.070	0.030	CP1W-TS003
		12CH		Sensor type: Thermocouple	(Lor K)	0.080	0.050	CP1W-TS004

I/O Connecting Cable

Product name	Specifications	Model
I/O Connecting Cable	80 cm (for CP1W Expansion I/O Units and Expansion Units) Only one I/O Connecting Cable can be used in each PLC.	CP1W-CN811

Recommended Ethernet Communications Cables

For the Ethernet communications cable specifications, refer to page 26.

	Item	Recommended manufacturer	Cable length (m)	Model
	Cable with Connectors on Both Ends	OMRON	0.3	XS6W-6LSZH8SS30CM-Y
	(RJ45/RJ45) Standard RJ45 plug type *1		0.5	XS6W-6LSZH8SS50CM-Y
Wire Gauge and Number of Pairs:	Cable color: Yellow *3		1	XS6W-6LSZH8SS100CM-Y
AWG26, 4-pair Cable Cable Sheath material: LSZH *2			2	XS6W-6LSZH8SS200CM-Y
			3	XS6W-6LSZH8SS300CM-Y
			5	XS6W-6LSZH8SS500CM-Y
	Cable with Connectors on Both Ends	OMRON	0.3	XS5W-T421-AMD-K
	(RJ45/RJ45) Rugged RJ45 plug type *1		0.5	XS5W-T421-BMD-K
Wire Gauge and Number of Pairs:	Cable color: Light blue		1	XS5W-T421-CMD-K
AWG22, 2-pair cable	All I		2	XS5W-T421-DMD-K
	20		5	XS5W-T421-GMD-K
			10	XS5W-T421-JMD-K

^{*1.} Cables with standard RJ45 plugs are available in the following lengths: 0.2 m, 0.3 m, 0.5 m, 1 m, 1.5 m, 2 m, 3 m, 5 m, 7.5 m, 10 m, 15 m, 20 m. Cables with rugged RJ45 plugs are available in the following lengths: 0.3 m, 0.5 m, 1 m, 2 m, 3 m, 5 m, 10 m, 15 m. For details, refer to the Industrial Ethernet Connectors Catalog (Cat. No. G019).

RS-232C Connecting Cable

Name	Specifications	Model	
	Connection cable with programmable terminal NB / NS	Length: 2 m	XW2Z-200T
PT-to-PLC	For CP2E-N+CP1W-CIF01	Length: 5 m	XW2Z-500T
Connecting Cable	Connection cable with programmable terminal NB / NS	Length: 2 m	XW2Z-200T-3
	For CP2E-S/E, CP2E-N+CP2W-CIFD1/CIFD2	Length: 5 m	XW2Z-500T-3

DIN Track Accessories

	Name	Specifications	Model
		Length: 0.5 m; Height: 7.3 mm	PFP-50N
DI	N Track	Length: 1 m; Height: 7.3 mm	PFP-100N
		Length: 1 m; Height: 16 mm	PFP-100N2
	End Plate	A stopper to secure the Units on the DIN Track.	PFP-M

Programming Devices

Software

	Specifications			
Product name		Number of licenses	Media	Model
FA Integrated Tool Package CX-One Lite Ver.4.□	CX-One Lite is a subset of the complete CX-One package that provides only the Support Software required for micro PLC applications. CX-One Lite Ver. 4. includes Micro PLC Edition CX-Programmer Ver.9. □.	1 license	DVD	CXONE-LT01D-V4
FA Integrated Tool Package CX-One Package Ver. 4.□	CX-One is a comprehensive software package that integrates Support Software for OMRON PLCs and components. CX-One Ver. 4.□ includes CX-Programmer Ver. 9.□.	1 license * 1	DVD	CXONE-AL01D-V4

^{*2.} The lineup features Low Smoke Zero Halogen cables for in-cabinet use and PUR cables for out-of-cabinet use. Although the LSZH cable is single shielded, its communications and noise characteristics meet the standards.

^{*3.} Cable colors are available in yellow, green, and blue.

^{*4.} For details, contact your OMRON representative.

<sup>Note: 1. For details, refer to the CX-One Catalog (Cat. No. R134), visit your local OMRON website.
2. CP2E CPU Units are supported by CX-One version 4.51 or higher and CX-Programmer version 9.72 or higher.</sup>

^{3.} The CX-One and CX-One Lite cannot be simultaneously installed on the same computer.

^{*1.} Multi licenses (3, 10, 30, or 50 licenses) and DVD media without licenses are also available for the CX-One.

CP2E

General Specifications

Item		AC power supply	DC power supply			
Model		CP2E-□□□□□-A	CP2E-□□□D□-D			
Enclosure		Mounted in a panel				
Dimensions (H	× D × W)	CPU Unit with 14 or 20 I/O points (CP2E-□14/20D□-□): 90mm *1 ×80mm *2 × 86mm CPU Unit with 30 I/O points (CP2E-□30D□-□): 90mm *1 × 80mm *2 × 130mm CPU Unit with 40 I/O points (CP2E-□40D□-□): 90mm *1 × 80mm *2 × 150mm CPU Unit with 60 I/O points (CP2E-□60D□-□): 90mm *1 × 80mm *2 × 195mm				
Weight		CPU Unit with 14 I/O points (CP2E-\[\] 14D\[\] -\[\]): 335g m CPU Unit with 20 I/O points (CP2E-\[\] 20D\[\] -\[\]): 340g m CPU Unit with 30 I/O points (CP2E-\[\] 30D\[\] -\[\]): 580g m CPU Unit with 40 I/O points (CP2E-\[\] 40D\[\] -\[\]): 640g m CPU Unit with 60 I/O points (CP2E-\[\] 60D\[\] -\[\]): 780g m	nax. nax. nax.			
	Supply voltage	100 to 240 VAC 50/60 Hz	24 VDC			
	Operating voltage range	85 to 264 VAC	20.4 to 26.4 VDC			
	Power consumption	15 VA/100 VAC max. 25 VA/240 VAC max. (CP2E-□14/20D□-A)	13W max. (CP2E-□14/20D□-D)			
Electrical specifications	Power consumption	50 VA/100 VAC max. 70 VA/240 VAC max. (CP2E-□30/40/60D□-A)	20W max. (CP2E-□30/40/60D□-D) * 4			
	Inrush current	120 VAC, 20 A for 8 ms max. for cold start at room temperature 240 VAC, 40 A for 8 ms max. for cold start at room temperature	24 VDC, 30A for 20 ms max. for cold start at room temperature			
	External power supply *3	Not provided. (CP2E-□14/20D□-A) 24 VDC, 300 mA (CP2E-□30/40/60D□-A)	Not provided.			
	Insulation resistance	$20~\text{M}\Omega$ min. (at 500 VDC) between the external AC terminals and GR terminals	Not csolated between primary and secondary DC power supplies			
	Dielectric strength	2,300 VAC 50/60Hz for 1 min between AC external and GR terminals Leakage current: 5 mA max.	Not csolated between primary and secondary DC power supplies			
	Power interrupt time	10 ms min.	2 ms min.			
	Ambient operating temperature	-20 to 60°C				
	Ambient humidity	10% to 90%				
	Atmosphere	No corrosive gas.				
	Ambient storage temperature	-20 to 75°C (excluding battery)				
	Altitude	2,000 m max.				
pplication	Pollution degree	2 or less: Conforms to IEC61010-2-201.				
nvironment	Noise resistance	2 kV on power supply line (Conforms to IEC61000-4-4.)			
	Overvoltage category	Category II: Conforms to IEC61010-2-201.				
	EMC immunity level	Zone B				
Vibration resistance Shock resistance		Conforms to IEC60068-2-6. 5 to 8.4 Hz with 3.5-mm amplitude, 8.4 to 150 Hz Acceleration of 9.8 m/s² for 100 min in X, Y, and Z directions (10 sweeps of 10 min each = 100 min total)				
		Conforms to IEC60068-2-27. 147 m/s², 3 times in X, Y, and Z directions				
erminal block		Fixed (not removable)				
erminal screw	size	M3				
pplicable stan	dards	Conforms to EC Directives, KC, UL and EAC.				
Frounding meth	nod	Ground to 100Ω or less.				

^{*1.} Total of 110 mm with mounting brackets.

^{*2.} Excluding cables.

^{*3.} Use the external power supply to power input devices. Do not use it to drive output devices.

*4. This is the rated value for the maximum system configuration. Use the following formula to calculate power consumption for CPU Units with

Formula: DC power consumption = (5V current consumption × 5 V/70% (internal power efficiency) + 24V current consumption) × 1.1(current fluctuation factor)

The above calculation results show that a DC power supply with a greater capacity is required.

Note: 1. The Expansion I/O Units and Expansion Units work under the same conditions as the CPU Units unless otherwise specified.

Performance Specifications

Item			CP2E-E□□D□-□	CP2E-S□□D□-□	CP2E-N□□D□-□	
Program capacity		4K steps	8K steps	10K steps		
FB capacity		4K steps	8K steps	10K steps		
Control method	d		Stored program method	Stored program method		
/O control met	hod		Cyclic scan with immediate re	efreshing		
Program langu	age		Ladder diagram			
Function blocks		Maximum number of function Maximum number of instance Languages usable in function		nms, structured text (ST)		
nstructions			Approximately 220			
D	Overhead proce	essing time	0.1 ms	0.15 ms	0.2 ms	
Processing speed	Instruction exec	cution times	LD 0.23 μs MOV 1.76 μs			
	IW-series Expansi Insion Units conn		CP2E-\(\Bar{14}/20D\(\Bar{1}\)-\(\Bar{1}\): None CP2E-\(\Bar{30}/40/60D\(\Bar{1}\)-\(\Bar{1}\): 3 un	its		
Maximum num	ber of I/O points		CP2E14D: 14 CP2E20D: 20 CP2E30D: 150 (30 bu CP2E40D: 160 (40 bu CP2E60D: 180 (60 bu	ilt in, 40 × 3 expansion)		
Built-in input function	High-speed counters	High-speed counter mode/maximum frequency	Incremental Pulse Inputs 100 kHz: 2 counters 10 kHz: 4 counters Up/Down Inputs 100 kHz: 1 counter 10 kHz: 1 counter 10 kHz: 2 counter 100 kHz: 2 counters Differential Phase Inputs 50 kHz: 1 counter 5 kHz: 1 counter		N14/20D□-□ Incremental Pulse Inputs 100 kHz: 2 counters 10 kHz: 4 counters 100 kHz: 1 counter 100 kHz: 1 counter 100 kHz: 1 counter 100 kHz: 1 counter 100 kHz: 2 counters Differential Phase Inputs 100 kHz: 1 counter Siderential Phase Inputs 100 kHz: 1 counter N30/40/60D□-□ Incremental Pulse Inputs 100 kHz: 3 counters 10 kHz: 3 counters 10 kHz: 3 counters 10 kHz: 2 counters 100 kHz: 2 counters Differential Phase Inputs 100 kHz: 2 counters Differential Phase Inputs 100 kHz: 2 counters	
		Counting mode	Linear mode Ring mode			
		Count value	32 bits			
		Counter reset modes	Phase Z and software reset Software reset	et (excluding increment pulse inp	out)	
		Control method	Target matching Range comparison			
	Input interrupts		6 inputs		8 inputs (6 inputs only for 14 I/O points	
			Interrupt input pulse width: 50) μs min.		
	Quick-response	e inputs	6 inputs		8 inputs (6 inputs only for 14 I/O points	
	4		Input pulse width: 50 µs min.		1	
Normal input Input constants			Delays can be set in the PLC setup (0 to 32 ms, default: 8 ms). Set values: 0, 1, 2, 4, 8, 16, or 32 ms			

CP2E

Item			CP2E-E□□D□-□	CP2E-S□□D□-□	CP2E-N□□D□-□	
		Pulse output		Pulse + Direction Mode	+	
		Frequency		1 Hz to 100kHz : 2 outputs	N14/20D□-□ 1 Hz to 100kHz: 2 outputs N30/40/60D□-□ 1 Hz to 100kHz: 4 outputs	
		Output mode		Continuous mode (for speed Independent mode (for posit		
	Pulse outputs (Models with transistor	Number of output pulses	Pulse output function not included	Relative coordinates: 0000 0000 to 7FFF FFFF hex (0 to 2147483647) Absolute coordinates: 8000 0000 to 7FFF FFFF hex (-2147483647 to 2147483647)		
Built-in output	outputs only)	Acceleration/ deceleration curves		Trapezoidal acceleration and de acceleration and deceleration).	eceleration (Cannot perform S-curve	
function		Changing SVs during instruction execution		Only target position can be cha	nged.	
		Origin searches		Included		
		Linear interpolation		None	N14/20D□-□ 2 axes max. N30/40/60D□-□ 4 axes max.	
	PWM output	Frequency		2.0 to 6,553.5 Hz (in increments of 0.1 Hz) with 1 output or 2 Hz 32,000 Hz (in increments of 1 Hz) with 1 output		
	(transistor outputs models only)	Duty factor	PWM output function not included	0.0% to 100.0% (in increments of 0.1%) Accuracy: +1%/-0% at 2 Hz to 10,000 Hz and +5%/-0% at 10,000 to 32,000 kHz		
	Output mode		Continuous Mode			
	Peripheral USB	port	Conforming to USB 2.0 B-type	conntor	- None	
		Transmission distance	5 m max.		None	
	Built-in RS232C port		Interface: Conforming to EIA RS	_		
		Transmission distance	15 m max.		_	
		Communications method	Half duplex			
	Synchronization		Start-stop			
		Baud rate	1.2, 2.4, 4.8, 9.6, 19.2, 38.4, 57	.6, or 115.2 kbps	None	
Communicatio		Supported protocol	Host Link 1:N NT Link No-protocol mode Serial PLC Links (master, slave) Modbus-RTU Easy Master Modbus-RTU Slave			
ns	Built-in RS485 p (not isolated)	port		Interface: Conforming to EIA RS-485		
		Transmission distance		50 m max.		
		Communications method		Half duplex		
		Synchronization		Start-stop]	
		Baud rate	None	1.2, 2.4, 4.8, 9.6, 19.2, 38.4, 57.6, or 115.2 kbps	None	
		Supported protocol		Host Link 1:N NT Link No-protocol mode Serial PLC Links (master, slave) Modbus-RTU Easy Master Modbus-RTU Slave		

Item			CP2E-E□□D□-□	CP2E-S□□D□-□	CP2E-N□□D□-□
		Number of Option Boards			N14/20D□-□ 1 Option Board N30/40/60D□-□ 2 Option Boards
		Number of serial communications			N14/20D□-□ 2 ports max. N30/40/60D□-□ 3 ports max.
		Communications method			Depends on Option Board
		Synchronization			Depends on Option Board
		Baud rate			1.2, 2.4, 4.8, 9.6, 19.2, 38.4, 57.6, or 115.2 kbps
Communications	Serial Option port	Mountable Option Boards	None		Serial Communication Option Board with one port One RS-232C port: CP1W-CIF01 (Start-stop) One RS-422A/485 port (not isolated): CP1W-CIF11 (Start-stop) One RS-422A/485 port (isolated): CP1W-CIF12-V1 (Start-stop) Serial Communication Option Board with two ports *1 Two RS-232C ports: CP2W-CIFD1 (Start-stop) One RS-232C port and one RS-485 port (isolated): CP2W-CIFD2 (Start-stop) Two RS-485 ports (isolated): CP2W-CIFD3 (Start-stop) Analog Option Board *2 CP1W-MAB221/ADB21/DAB21V *1. CP2W-CIFD□ can only be mounted on option slot 1. *2. Maximum one Analog Option Board can be mounted on an N□□-type CPU Unit.
TIS .		Compatible protocols			Host Link* 1:N NT Link* No-protocol mode Serial PLC Links (master, slave) Modbus-RTU Easy Master Modbus-RTU Slave PORT1 (EX) is not supported.
		Physical layer			100/10BASE-TX (Auto-MDIX)
		Media access methiod			CSMA/CD
		Modulation			Baseband
	Ethernet	Baud rate			100BASE-TX: 100Mbit/s 10BASE-T: 10Mbit/s • Half/full auto-negotiation for each port • Link speed auto-sensing for each port
		Transmission media	None		100BASE-TX Unshielded twisted-pair (UDP) cable Categories: 5, 5e Shielded twisted-pair (STP) cable Categories: 100Ω at 5, 5e 10BASE-T Unshielded twisted-pair (UDP) cable Categories: 3, 4, 5, 5e Shielded twisted-pair (STP) cable Categories: 100Ω at 3, 4, 5, 5e
		Transmission distance			100 m (distance between switch and node)
		Protocol			TCP, UDP, APR, ICMP (ping only), SNTP, DNS
		Applications			FINS, Socket, SNTP, DNS (Client)
		Number of Ethernet ports			N14/20: 1 port N30/40/60: 2 ports
		Ethernet switch			Layer 2 switch * N14/20 is not supported.

CP2E

Item		CP2E-E□□D□-□	CP2E-S□□D□-□	CP2E-N□□D□-□		
Number of tasks		 17 1 cyclic task 16 interrupt tasks Scheduled interrupt task: Interrupt task 1 (fixed) Built-in input interrupt task: Interrupt task 2 to 9 (IN8 and IN9 can only be used in N20/30/40/60 CPU Units) High-speed counter interrupt task: Interrupt task 1 to 16 				
Maximum subr	outine number	128				
Maximum jump	number	128				
Scheduled interrupt tasks		1 interrupt task				
Battery service life *With CP2W-BAT02 Battery (optional)		Battery cannot be mounted.				
Clock		None	Supported. Accuracy (monthly deviation): -4.5 min to -0.5 min (ambient to -2.0 min to +2.0 min (ambient to -2.5 min to +1.5 min to -2.5 min to +1.5 min to -2.5 min (ambient to -2.5 min to +1.5 min to -2.5 min to -2.5 min to +1.5 min to -2.5 min to +1.5 min to -2.5 min to -4.5 min to -2.5 min to -4.5 min to -2.5 min to -4.5 mi	temperature: 25°C),		
Memory backup Built-in Flash Memory Built-in non-volatile memory		Ladder programs and parameters are automatically saved to built-in Flash Memory. A section of the Data Memory Area can be saved to the built-in Flash Memory.				
		Data Memory Area (D), Holding Area (H), Counter Area (C) and Auxiliary Area (A) are automatically saved to the built-in non-volatile memory.				
	Input Bits	1,600 bits (100 words): CIO 0.00 to CIO 99.15 (CIO 00 to CIO 99)				
CIO Area	Output Bits	1,600 bits (100 words): CIO 100.00 to CIO 199.15 (CIO 100 to CIO 199)				
	Serial PLC Link Words	1,440 bits (90 words): CIO 200.00 to CIO 289.15 (CIO 200 to CIO 289)				
Work Area (W)		2,048 bits (128 words): W0.00 to W127.15 (W0 to W127)				
Holding Area (I	1)	2,048 bits (128 words): H0.00 to H127.15 (H0 to 127) Words H512 to H1535: These words can be used only for function blocks.				
Auxiliary Area	(A)	Read-only: 7,168 bits (448 words): A0.00 to A447.15 (A0 to A447) Read/write: 8,192 bits (512 words): A448.00 to A959.15 (A448 to A959)				
Temporary Are	a (TR)	16 bits: TR0 to TR15				
Timer Area (T)		256 timer numbers (T0 to T255 (separate from counters)) Words T256 to T511: These words can be used only for function blocks.				
Counter Area (C)		C255 (separate from timers)) words can be used only for functio	n blocks.		
Data Memory Area (D)		4 K words: D0 to D4095 DM backup: 1,500 words (D0 to D1499)	8 K words: D0 to D8191 DM backup: 7,000 words (D0 to D6999)	16 K words: D0 to D16383 DM backup: 15,000 words (D0 to D14999)		
Index Registers	s (IR)	16 registers: IR0 to IR15				
Data Registers (DR)		16 registers: DR0 to DR15				
Operating modes		PROGRAM Mode: Program execution is stopped. Preparations can be executed prior to program execution in this mode. MONITOR Mode: Programs are executed. Some operations, such as online editing, and changes to present values in I/O memory, are enabled in this mode. RUN Mode: Programs are executed. This is the normal operating mode.				

Allocating Built-in Input Terminals to Functions

Input terminals are allocated functions by setting parameters in the PLC Setup. Set the PLC Setup so that each terminal is used for only one function.

E20/30/40/60, S30/40/60 or N20/30/40/60 CPU Units

		PLC Setup							
Terminal	Terminal		rupt input s ilt-in Input T		High-speed counter 0 to 5 settings on Built-in Input Tab Page			Origin search settings on Pulse Output 0 to 3 Tab Page	
block label	number	Normal	Interrupt	Quick		Use		Use	
		Normal input	Interrupt inputs	Quick-response inputs	Increment pulse input	Differential phase ×4 or up/down	Pulse/ direction	Origin search	
	00	Normal input 0			Counter 0, increment input	Counter 0, phase A or up input	Counter 0, pulse input		
	01	Normal input 1			Counter 1, increment input	Counter 0, phase B or down input	Counter 1, pulse input		
	02	Normal input 2	Interrupt input 2	Quick-response input 2	Counter 2, increment input	Counter 1, phase A or up input	Counter 0, direction		
	03	Normal input 3	Interrupt input 3	Quick-response input 3		Counter 1, phase B or down input	Counter 1, direction		
	04	Normal input 4	Interrupt input 4	Quick-response input 4	Counter 3, increment input	Counter 0, phase Z or reset input	Counter 0, reset input		
CIO 0	05	Normal input 5	Interrupt input 5	Quick-response input 5	Counter 4, increment input	Counter 1, phase Z or reset input	Counter 1, reset input		
	06	Normal input 6	Interrupt input 6	Quick-response input 6	Counter 5, increment input			Pulse 0, Origin input signal	
	07	Normal input 7	Interrupt input 7	Quick-response input 7				Pulse 1, Origin input signal	
	08	Normal input 8	Interrupt input 8 *1	Quick-response input 8 *1				Pulse 2, Origin input signal *2	
	09	Normal input 9	Interrupt input 9 *1	Quick-response input 9 *1				Pulse 3, Origin input signal *2	
	10	Normal input 10						Pulse 0, Origin proximity input signal	
	11	Normal input 11						Pulse 1, Origin proximity input signal	
CIO 1	00	Normal input 12						Pulse 2, Origin proximity input signal *2	
	01	Normal input 13						Pulse 3, Origin proximity input signal *2	
	02 to 11	Normal input 14 to 23							
CIO 2	00 to 11	Normal input 24 to 35							

Note: 1. The same pulse inputs must be used for high-speed counter 0 and high-speed counter 1.

2. High-speed counter 2 cannot be used if the input setting of high-speed counter 0 or high-speed counter 1 is set for differential phase inputs (4×), pulse + direction inputs, or up/down pulse inputs.

E14 or N14 CPU Units

					PLC Set	tup		
Terminal	Terminal	Interrupt input settings erminal on Built-in Input Tab Page		High-speed counter 0 to 5 settings on Built-in Input Tab Page			Origin search settings on Pulse Output 0/1 Tab Page	
block label	number	Normal	Interrupt	Quick		Use		Use
labei		Normal input	Interrupt inputs	Quick-response inputs	Increment pulse input	Differential phase ×4 or up/down	Pulse/ direction	Origin search
	00	Normal input 0			Counter 0, increment input	Counter 0, phase A or up input	Counter 0, pulse input	
	01	Normal input 1			Counter 1, increment input	Counter 0, phase B or down input	Counter 1, pulse input	
	02	Normal input 2	Interrupt input 2	Quick-response input 2	Counter 2, increment input	Counter 1, phase A or up input	Counter 0, direction	
CIO 0	03	Normal input 3	Interrupt input 3	Quick-response input 3		Counter 1, phase B or down input	Counter 1, direction	Pulse 0, Origin proximity input signal
CIO U	04	Normal input 4	Interrupt input 4	Quick-response input 4	Counter 3, increment input	Counter 0, Phase Z or reset input	Counter 0, reset input	
	05	Normal input 5	Interrupt input 5	Quick-response input 5	Counter 4, increment input	Counter 1, Phase Z or reset input	Counter 1, reset input	Pulse 1, Origin proximity input signal
	06	Normal input 6	Interrupt input 6	Quick-response input 6	Counter 5, increment input			Pulse 0, Origin input signal
	07	Normal input 7	Interrupt input 7	Quick-response input 7				Pulse 1, Origin input signal

Note: 1. The same pulse inputs must be used for high-speed counter 0 and high-speed counter 1.

2. High-speed counter 2 cannot be used if the input setting of high-speed counter 0 or high-speed counter 1 is set for differential phase inputs (4×), pulse + direction inputs, or up/down pulse inputs.

^{*1.} Only supported by N□□-type CPU Units. ***2.** Only supported by N30/40/60 CPU Units.

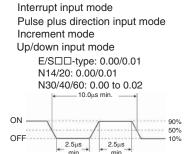
CP2E

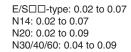
I/O Specifications

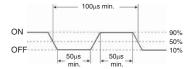
Specifications

Item			Specification				
Input type		High-speed counter inputs or normal inputs	High-speed counter inputs, interrupt inputs, quick-response inputs or normal inputs	Normal inputs			
	E/S□□-type and N14 CPU Units	CIO 0.00 and CIO 0.01	CIO 0.02 to CIO 0.07	CIO 0.08 to CIO 0.11, CIO 1.00 to CIO 1.11 and CIO 2.00 to CIO 2.11 *1			
Input bits	N20 CPU Units	CIO 0.00 and CIO 0.01	CIO 0.02 to CIO 0.09	CIO 0.10 to CIO 0.11			
	N30/40/60 CPU Units	CIO 0.00 to CIO 0.03	CIO 0.04 to CIO 0.09	CIO 0.10, CIO 0.11, CIO 1.00 to CIO 1.11 and CIO 2.00 to CIO 2.11 *1			
Applicable	inputs	2-wire and 3-wire sensors					
Input voltag	ge	24 VDC, +10% / -15%					
Input imped	dance	$3.3 \text{ k}\Omega$ $3.3 \text{ k}\Omega$		4.8 kΩ			
Input curre	nt	7.5 mA (typical)	7.5 mA (typical)	5 mA (typical)			
ON voltage	/current	17.0 VDC min. / 3 mA min.	17.0 VDC min. / 3 mA min.	14.4 VDC min. / 3 mA min.			
OFF voltag	e/current	5.0 VDC max. / 1 mA max.	5.0 VDC max. / 1 mA max.	5.0 VDC max. / 1 mA max.			
ON respons	se time * 2	2.5 μs min.	50 μs max.	1 ms max.			
OFF respon	nse time * 2	2.5 μs min.	50 μs max.	1 ms max.			
Circuit configuration		r	Input indicator Input indicator Internal circuits COM				

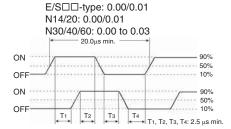
- *1. The bits that can be used depend on the model of CPU Unit.
- *2. The response time is the delay caused by hardware. The delay set in the PLC Setup (0 to 32 ms, default: 8 ms) for a normal input must be added to this value.



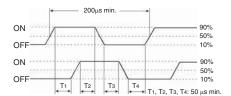




Differential phase mode



E/S - type: 0.02/0.03 N14/20: 0.02/0.03



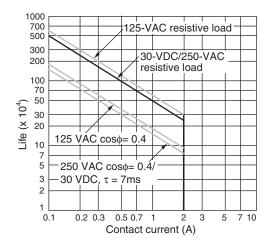
Output Specifications for Relay Outputs

CP2E-□□□DR-□

Item			Specification	
Maximum switching capacity		city	2 A 250 VAC (cosφ= 1) 2 A 24 VDC (4 A/common)	
Minimum swi	itching capad	city	10 mA 5 VDC	
	Electrical	Resistive load	200,000 operations (24 VDC)	
Service life of relay	Service life effective load Inductive load		70,000 operations (250 VAC, cosφ = 0.4)	
o. relay	Mechanica	l	20,000,000 operations	
ON response	time		15 ms max.	
OFF respons	e time		15 ms max.	
Circuit configuration			Output indicator Output indicator	

Estimating the Service Life of Relays

Under normal conditions, the service life of output contacts is as shown above. The service life of relays is as shown in the following diagram as a guideline.



Output Specifications for Transistor Outputs (Sinking or Sourcing)

CP2E-N14/20/30/40/60DT(1)-□, CP2E-S30/40/60DT(1)-□

Normal Outputs

	Speci	ification
Item	S□□-type: CIO 100.00 and CIO 100.01 N□□-type: CIO 100.00, CIO 100.01, CIO 101.00 and CIO 101.01	S□□-type: CIO 100.02 to CIO 102.07 *2 N□□-type: CIO 100.02 to CIO 100.07, CIO 101.02 to CIO 102.07 *2
Maximum switching capacity	0.3 A/output, 0.9 A/common *1 4.5 to 30 VDC CP2E-N14D: 1.5 A/Unit CP2E-N20D: 1.8 A/Unit CP2E-S/N30D: 2.7 A/Unit	
Minimum switching capacity	1 mA 4.5 to 30 VDC	
Leakage current	0.1mA max.	
Residual voltage	0.6 V max.	1.5V max.
ON response time	0.1 ms max.	0.1 ms max.
OFF response time	0.1 ms max.	1 ms max.
Fuse	Not provided.	
External power supply	20.4 to 26.4VDC 30mA max. (N□□-type is not needed)	Not needed
Circuit configuration	Sourcing output model Internal circuits Note: The property of the property	Sinking output model Internal circuits

*1. Also do not exceed 0.9 A for the total of CIO 100.00 to CIO 100.03, which are different common.

*2. The bits that can be used depend on the model of CPU Unit.

Note: 1. Do not connect a load to an output terminal or apply a voltage in excess of the maximum switching capacity.

Pulse Outputs

	Specification		
Item	S□□-type: CIO 100.00 and CIO 100.01 N□□-type: CIO 100.00, CIO 100.01, CIO 101.00 and CIO 101.01		
Maximum switching capacity	100 mA 4.5 to 26.4 VDC		
Minimum switching capacity	7 mA 4.5 to 26.4 VDC		
Maximum output frequency	100 kHz		
Output waveform	OFF 90% ON 10% 4μs min. 2μs min. The OFF and ON refer to the output transistor. The output transistor is ON at level "L".		

- Note: 1. The load for the above values is assumed to be the resistive load, and does not take into account the impedance for the connecting cable to the load.
 - 2. Due to distortions in pulse waveforms resulting from connecting cable impedance, the pulse widths in actual operation may be smaller than the values shown above.

PWM Output (CIO 100.01)

Item	Specification			
Maximum switching capacity	30 mA 4.5 to 26.4 VDC			
Maximum output frequency	32 kHz			
PWM output accuracy	For ON duty +1%, -0%:10 kHz output For ON duty +5%, -0%: 0 to 32 kHz output			
Output waveform	OFF ON The OFF and ON refer to the output transistor. The output transistor is ON at level "L".			

CP2E

Built-in Ethernet

General Specifications (Ethernet)

Item		Specif	ications				
Туре		100BASE-TX (Auto-MDIX)	10BASE-T (Auto-MDIX)				
Number of Ethernet ports		N14/20 CPU Units: 1 port N30/40/60 CPU Units: 2 ports (Switching Hub function is built in.)					
	Media access method	CSMA/CD					
	Modulation method	Baseband	Baseband				
	Transmission paths	Star form					
	Bourd mate	100 Mbit/s (100Base-TX) Auto-Negotiation	10 Mbit/s (10Base-T) Auto-Negotiation				
Transfer	Baud rate	Half/full auto-negotiation for each port Link speed auto-sensing for each port	•				
	Transmission media	 Unshielded twisted-pair (UDP) cable Categories: 5, 5e Shielded twisted-pair (STP) cable Categories: 100Ω at 5, 5e 	 Unshielded twisted-pair (UDP) cable Categories: 3, 4, 5, 5e Shielded twisted-pair (STP) cable Categories: 100Ω at 3, 4, 5, 5e 				
	Transmission distance	100 m (distance between hub and node)					
Protocols		TCP, UDP, ARP, ICMP (ping only), SNTP, DNS					

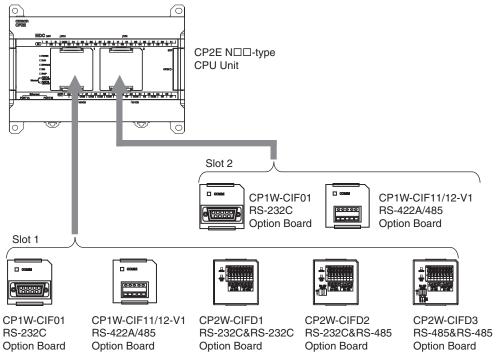
FINS Communications Service Specifications

Item		Specification				
Protocol name	FINS/UDP FINS/TCP					
Number of nodes	254					
Message Length	1016 bytes max.					
Date Length	1004 bytes max.					
Number of buffer	16	16				
	UDP/IP	TCP/IP				
Protocol used		The selection of UDP/IP or TCP/IP is made by means of the FINS/UDP or FINS/TCP button in Built-in Ethernet Tab in the CX-Programmer's PLC Setup.				
Number of connections		3 for user, 1 for CX-Programmer auto connection				
Port number	9600 (default) Can be changed. 9600 (default) Can be changed.					
Protection	No	No Yes (Specification of client IP addresses when unit is used as a server)				
Local IP address	192.168.250.FINS node address					

Switching Hub for CP2E N□□-type CPU Units

Ethernet	100Base-TX, 10Base-T
Auto MID/MID-X	Yes
Auto negotiation	Yes
Store-and-forward system	Yes
Buffer	32K bytes
MAC address	1000
Broadcast storm detection	Yes
QoS	No
SNMP	No
VLAN	No
IGMP snooping	No
STP (Spanning Tree Protocol)	No
Port mirroring	No

Serial Communication



Note: 1. CP2W-CIFD□ can only be mounted on option slot 1.

Serial Communication Option Board

Model numbers	Port	Maximum transmission distance	Connection method
CP1W-CIF01	One RS-232C port	15m	Connector (D-sub, 9 pin female)
CP1W-CIF11	One RS-422A/485 port (not isolated)	50m	Terminal block (using ferrules)
CP1W-CIF12-V1	One RS-422A/485 port (isolated)	500m	Terminal block (using ferrules)
CP2W-CIFD1	Two RS-232C Ports	15m	Terminal block (using ferrules)
CP2W-CIFD2	One RS-232C port and one RS-485 port (isolated)	15m (RS-232C) 500m (RS-485)	Terminal block (using ferrules)
CP2W-CIFD3	Two RS-485 ports (isolated)	500m	Terminal block (using ferrules)

Built-in RS-232C Port for E/S□□-type CPU Units



Pin	Abbr.	Signal Name	Signal direction
1	SD(TXD)	Send data	Output
2	RD(RXD)	Receive data	Input
3	RS(RTS)	Request to send	Output
4	CS(CTS)	Clear to send	Input
5	SG(0V)	Signal ground	-
6	FG	Frame ground	_

Built-in RS-485 Port (2-wire) for S□□-type CPU Units

RS-485 Terminal Block

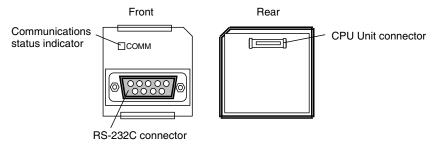


Pin	Abbr.	Signal Name	Signal direction
1	A-	Send/Receive data -	-
2	B+	Send/Receive data +	-
3	FG	Frame ground	-

DIP Switch for Terminating Resistance Settings

	Setting						
ONû	ON	OFF					
	OFF	OFF	Terminating resistance selection Resistance value: 220 Ω typical				

CP1W-CIF01 RS-232C Option Board



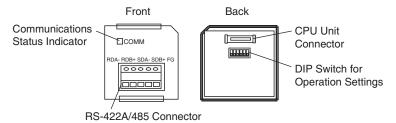
RS-232C Connector



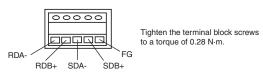
Pin	Abbr.	Signal	Signal direction
1	FG	Frame ground	-
2	SD(TXD)	Send data	Output
3	RD(RXD)	Receive data	Input
4	RS(RTS)	Request to send	Output
5	CS(CTS)	Clear to send	Input
6	5V	Power	-
7	DR(DSR)	Data set ready	Input
8	ER(DTR)	Data terminal ready	Output
9	SG(0V)	Signal ground	-
Connector hood	FG	Frame ground	-

Note: 1. The NV3W-M□20L-V1 Programmable Terminal can be connected to pin 6 (+5V) on the RS-232C Option Board (CP1W-CIF01) mounted to the CPU Unit. Do not connect pin 6 to any other device.

CP1W-CIF11/CIF12-V1 RS-422A/485 Option Board



RS-422A/485 Terminal Block

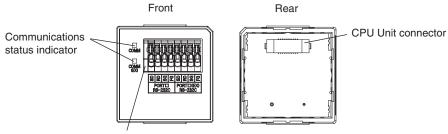


DIP Switch for Operation Settings

CP1W-CIF11		CP1W-CIF12-	·V1		CoMina		
	Pin		Pin	Setting			
	1		SW1	ON	ON (both ends)	Terminating resistance selection	
	1	SW1		OFF	OFF	Resistance value: 220Ω typical	
	2	OZ	2	ON	2-wire connections	2 wire or 4 wire collection std	
	2	linānni	2	OFF	4-wire connections	2-wire or 4-wire selection *1	
SW	3		1 0 0 4 3	ON	2-wire connections	2-wire or 4-wire selection *1	
0 Z	3	<u>-004</u>		OFF	4-wire connections	2-wire of 4-wire selection *1	
	4		4	-	-	Not used.	
-		SW2	1	ON	RS control enabled		
	5			OFF	RS control disabled (Data always received.)	RS control selection for RD *2	
				ON	RS control enabled		
	6 - 0	2	OFF	RS control disabled (Data always sent.)	RS control selection for SD *3		

- *1. Set both pins 2 and 3 to either ON (2-wire) or OFF (4-wire).
- *2. To disable the echo-back function, set pin 5 to ON (RS control enabled).
- ***3.** When connecting to a device on the N side in a 1: N connection with the 4-wire method, set pin 6 to ON (RS control enabled). Also, when connecting by the 2-wire method, set pin 6 to ON (RS control enabled).

CP2W-CIFD1 RS-232C&RS-232C Option Board



RS-232C&RS-232C terminal block

RS-232C&RS-232C Terminal Block

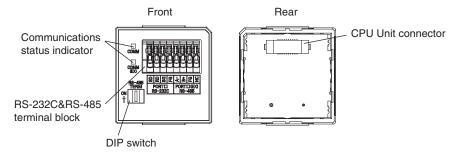


Port	Pin	Abbr.	Signal Name	Signal direction
	1	SD(TXD)	Send data	Output
PORT□	2	RD(RXD)	Receive data	Input
PORT	3	SG(0V)	Signal ground	
	4	FG	Frame ground	
	5	SD(TXD)	Send data	Output
DODT□ (EV)	6	RD(RXD)	Receive data	Input
PORT□ (EX)	7	SG(0V)	Signal ground	
	8	FG	Frame ground	

Note: 1. CP2W-CIFD1 can only be mounted on option slot 1.

PORT□ is supported by serial port 1 and PORT□ (EX) is supported by serial port 1(EX).

CP2W-CIFD2 RS-232C&RS-485 Option Board



RS-232C&RS-485 Terminal Block



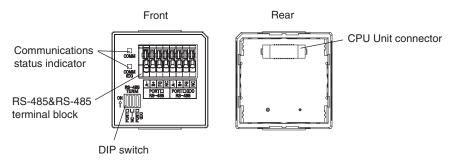
Port	Pin	Abbr.	Signal Name	Signal direction
	1	SD(TXD)	Send data	Output
DODT	2	RD(RXD)	Receive data	Input
PORT□	3	SG(0V)	Signal ground	
	4	FG	Frame ground	
	5	A-	Send/Receive data -	Output
PORT□ (EX)	6	B+	Send/Receive data +	Input
	7	FG	Frame ground	
	8	NC	NC	

Note: CP2W-CIFD2 can only be mounted on option slot 1.
PORT□ is supported by serial port 1 and PORT□ (EX) is supported by serial port 1(EX).

DIP switch for terminating resistance settings

Setting						
RS-485 TERM	ON	ON (both ends)	Terminating resistance selection			
ON T	OFF	OFF	Resistance value: 220Ω typical			

CP2W-CIFD3 RS-485&RS-485 Option Board



RS-485&RS-485 Terminal Block



Port	Pin	Abbr.	Signal Name	Signal direction
_	1	A-	Send/Receive data -	Output
PORT□	2	B+	Send/Receive data +	Input
PORT	3	FG	Frame ground	
	4	NC	NC	
	5	A-	Send/Receive data -	Output
DODT□ (EV)	6	B+	Send/Receive data +	Input
PORT□ (EX)	7	FG	Frame ground	
	8	NC	NC	

Note: 1. CP2W-CIFD3 can only be mounted on option slot 1.

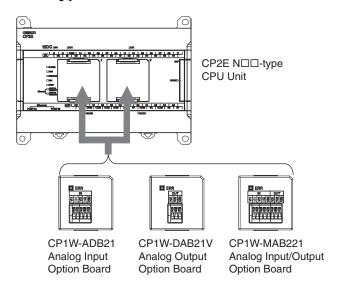
PORT□ is supported by serial port 1 and PORT□ (EX) is supported by serial port 1(EX).

DIP switch for terminating resistance settings

			3 -			
Pin			Setting			
DC_40E		ON	ON (both ends)	Terminating resistance selection		
RS-485 TERM	'	OFF	OFF	Resistance value: 220Ω typical		
ON ON	2			Not used.		
	3			Not used.		
S S S S S S S S S S S S S S S S S S S	4	ON	ON (both ends)	Terminating resistance selection		
	4	OFF	OFF	Resistance value: 220Ω typical		

Analog Option Board

N□□-type CPU Units



Note: 1. Maximum one Analog Option Board can be mounted on an N□□-type CPU Unit.

If two Analog Option Boards are mounted, an option board error will occur and both Analog Option Boards do not work.

Analog Option Board

Analog option board units are non-isolated analog units which allow you to easily realize analog input/output function for CP2E N□□-type CPU Unit.

Analog Option Board		Voltage Input 0V~10V (Resolution: 1/4000)	Current Input 0mA~20mA (Resolution: 1/2000)	Voltage Output 0V~10V (Resolution: 1/4000)
Analog I/O Option Board	CP1W-MAB221	2CH		2CH
Analog Input Option Board	CP1W-ADB21	2CH		
Analog Output Option Board	CP1W-DAB21V			2CH

Specifications of Analog Option Board CP1W-ADB21

Item		Specifi	cations	
item		Voltage Input	Current Input	
Input signal	range	0 to 10 V	0 to 20 mA	
Max. rated in	nput	0 to 15 V	0 to 30 mA	
External inpu	ıt impedance	200 k $Ω$ min.	Approx. 250 Ω	
Resolution		1/4000 (full scale)	1/2000 (full scale)	
	25°C	±0.5% (full scale)	±0.6% (full scale)	
Overall accuracy	0 to 60°C	±1.0% (full scale)	±1.2% (full scale)	
uoouiuoy	-20 to 0°C	±1.3% (full scale)	±1.5% (full scale)	
A/D convers	ion data	0000 to 0FA0 Hex	0000 to 07D0 Hex	
Averaging function		None		
Isolation method		No isolation between analog I/O terminals and internal circuits.		
Current cons	sumption	5 VDC: 20 mA max.		

CP1W-DAB21V

lta-m		Specifi	cations	
Item		Voltage Output	Current Output	
Output signa	al range	0 to 10 V		
External output allowable load resistance		2 kΩ min.		
External output impedance		0.5 Ω max.		
Resolution		1/4000 (full scale)		
_	25°C	±0.5% (full scale)		
Overall accuracy	0 to 60°C	±1.0% (full scale)		
accuracy	-20 to 0°C	±1.3% (full scale)		
Set data (D/A conversion)		0000 to 0FA0 Hex		
Isolation method		No isolation between analog I/O terminals and internal circuits.		
Current cons	sumption	5 VDC: 60 mA max.		

CP1W-MAB221

Item		Specifications			
item			Voltage I/O	Current I/O	
	Input signal ra	nge	0 to 10 V	0 to 20 mA	
	Max. rated inp	ut	0 to 15 V	0 to 30 mA	
	External input	impedance	200 kΩ min.	Approx. 250 Ω	
	Resolution		1/4000 (full scale)	1/2000 (full scale)	
Analog Input Section		25°C	±0.5% (full scale)	±0.6% (full scale)	
	Overall accuracy	0 to 60°C	±1.0% (full scale)	±1.2% (full scale)	
	uccuracy	-20 to 0°C	±1.3% (full scale)	±1.5% (full scale)	
	A/D conversion data		0000 to 0FA0 Hex	0000 to 07D0 Hex	
	Averaging function		None		
	Output signal range		0 to 10 VDC		
	External output allowable load resistance		2 kΩ min.		
	External outpu	ıt impedance	0.5 Ω max.		
Analog Output Section	Resolution		1/4000 (full scale)		
	_	25°C	±0.5% (full scale)		
	Overall accuracy	0 to 60°C	±1.0% (full scale)		
		-20 to 0°C	±1.3% (full scale)		
	Set data (D/A conversion)		0000 to 0FA0 Hex		
Isolation method			No isolation between analog I/O terminals and internal circuits.		
Current consumption			5 VDC: 80 mA max.		

Analog Option Board Refresh Time (Typical values)

Analog Oniton Poord	Cycle time (ms)				
Analog Opiton Board	1 ms	10 ms	20 ms		
CP1W-ADB21	16~40	20~60	20~100		
CP1W-DAB21V	9~37	26~58	46~86		
CP1W-MAB221(AD)	14~62	18~109	20~160		
CP1W-MAB221(DA)	9~53	26~102	46~150		

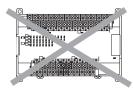
Specifications of Expansion I/O Units and Expansion Units

Expandable CPU Units

- Expansion I/O Units and Expansion Units cannot be connected to E14/20 or N14/20 CPU Units.
- A total of up to three Expansion I/O Units and Expansion Units can be connected to an E30/40/60, S30/40/60 or N30/40/60 CPU Unit.

CP2E-E14/20 or N14/20 CPU Unit

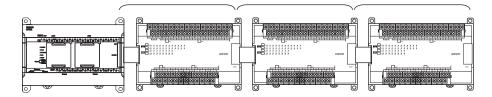




CP-series Expansion Units and Expansion I/O Units cannot be connected.

CP2E-E30/40/60, S30/40/60 or N30/40/60 CPU Unit

A total of up to three CP-series Expansion I/O Units and Expansion Units can be connected.



Connection Methods

Connection cables for the Expansion I/O Units and Expansion Units are used to connect the Units. The length can be extended by using a CP1W-CN811 I/O Connecting Cable (length: 800 mm).

Maximum Number of I/O Points for an Expansion I/O Unit or Expansion Unit

CPU Unit	Built-in I/O on CPU Unit		Total number of Expansion I/O Units and Expansion Units that can be connected	Number of inputs: 24 Number of outputs: 16 Total number of I/O points when three CP1W-40ED□ Expansion I/O Units are connected			
	Total	Number of inputs	Number of outputs	can be connected	Total	Number of inputs	Number of outputs
CP2E-□14D□-□	14	8	6	Not possible.	14	8	6
CP2E-□20D□-□	20	12	8		20	12	8
CP2E-□30D□-□	30	18	12	3 Units maximum	150	90	60
CP2E-□40D□-□	40	24	16		160	96	64
CP2E-□60D□-□	60	36	24		180	108	72

Specifications of Expansion I/O Units

Input Specifications (CP1W-8ED/20EDR1/20EDT/20EDT1/40EDR/40EDT/40EDT1)

Item	Specification				
Input voltage	24 VDC, +10% / -15%				
Input impedance	4.7 kΩ				
Input current	5 mA typical				
ON voltage / current	14.4 VDC min. / 3mA min.				
OFF voltage / current	5.0 VDC max. / 1mA max.				
ON response time	1 ms max. *1				
OFF response time	1 ms max. *1				
Circuit configuration	Input indicator Insolation circuits COMI				

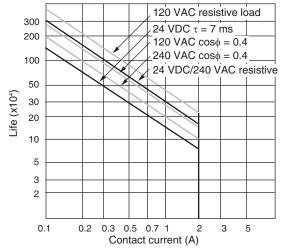
Note: 1. Do not apply voltage in excess of the rated voltage to the input terminal.

Output Specifications Relay Outputs (CP1W-8ER/16ER/20EDR1/32ER/40EDR)

Item			Specification	
Max. switching capacity			2 A, 250 VAC (cosφ = 1), 2 A, 24 VDC (4 A/common)	
Min. switching capa	city		10 mA, 5 VDC	
	Electrical	Resistive load	150,000 operations (24 VDC)	
Service life of relay	Electrical	Inductive load	100,000 operations (240 VAC, cosφ = 0.4)	
	Mechanical		20,000,000 operations	
ON response time			15 ms max.	
OFF response time			15 ms max.	
Circuit configuration			Output indicator OUT Internal circuits COM 250 VAC, 2A, 24 VDC, 2 A max.	

Note: 1. Estimating the Service Life of Relays

The service life of output contacts is as shown in the following diagram.



Switching frequency: 1,800 operations/hour

^{*1.} The response time is the delay caused by hardware. The delay set in the PLC Setup (0 to 32 ms, default: 8 ms) must be added to this value. For the CP1W-40EDR/EDT/EDT1, a fixed value of 16 ms must be added.

Transistor Outputs (Sinking or Sourcing)

		Specification							
Item	CP1W-40EDT CP1W-40EDT1	CP1W-32ET CP1W-32ET1	CP1W-20EDT CP1W-20EDT1	CP1W-16ET CP1W-16ET1	CP1W-8ET CP1W-8ET1				
Max. switching capacity	4.5 to 30 VDC 0.3 A/output	4.5 to 30 VDC 0.3 A/output	24 VDC +10%/-5% 0.3 A/output	4.5 to 30 VDC 0.3 A/output	4.5 to 30 VDC 0.3 A/output				
*1	0.9 A/common 3.6 A/Unit	0.9 A/common 7.2 A/Unit	0.9 A/common 1.8 A/Unit	0.9 A/common 3.6 A/Unit	0.9 A/common 1.8 A/Unit				
Leakage current	0.1 mA max.								
Residual voltage	1.5 V max.								
ON response time	0.1 ms max.	0.1 ms max.	0.1 ms.	0.1 ms max.	0.1 ms max.				
OFF response time	1 ms max. At 24 VDC +10%/ -5%, 5 to 300 mA	1 ms max. At 24 VDC +10%/ -5%, 5 to 300 mA	1 ms max. At 24 VDC +10%/ -5%, 5 to 300 mA	1 ms max. At 24 VDC +10%/ -5%, 5 to 300 mA	1 ms max. At 24 VDC +10%/ -5%, 5 to 300 mA				
Maximum number of simultaneously ON outputs	16 points (100% load)	24 points (100% load)	8 points (100% load)	16 points (100% load)	8 points (100% load)				
Fuse #2	1 fuse/common								
	Sinkin	g	Sourcing	9					

Circuit configuration

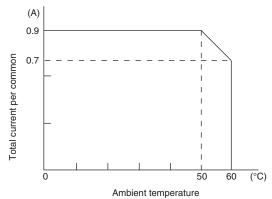
Sinking

Sourcing

Output indicator

Output

*1. If the ambient temperature is maintained below 50°C, up to 0.9 A/common can be used.



***2.** The fuse cannot be replaced by the user. Replace the Unit if the fuse breaks due to an short-circuit or overcurrent. **Note: 1.** Do not connect a load to an output terminal or apply a voltage in excess of the maximum switching capacity.

Specifications of Expansion Units Analog Input Units

Model		CP1W	/-AD041	CP1W	/-AD042		
Item		Voltage input	Current input	Voltage input	Current input		
Number of analog inputs		4 inputs (4 words allocated)	4 inputs (4 words allocated)				
Input signal range		0 to 5 V, 1 to 5 V, 0 to 10 V, or –10 to 10 V	0 to 20 mA or 4 to 20 mA	0 to 5 V, 1 to 5 V, 0 to 10 V, or -10 to 10 V	0 to 20 mA or 4 to 20 mA		
Max. rated input		±15 V	±30 mA	±15 V	±30 mA		
External input impedance)	1 MΩ min.	Approx. 250 Ω	1 MΩ min.	Approx. 250 Ω		
Resolution		1/6000 (full scale)	1/6000 (full scale)		•		
	25°C	0.3% full scale	0.4% full scale	0.2% full scale	0.3% full scale		
Overell conveni	0 to 55°C	0.6% full scale	0.8% full scale	0.5% full scale	0.7% full scale		
Overall accuracy	55 to 60°C	0.7% full scale	0.8% full scale	0.5% full scale	0.7% full scale		
	-20 to 0°C	0.8% full scale	1% full scale	0.7% full scale	0.9% full scale		
A/D conversion data		16-bit binary (4-digit hexadecimal) Full scale for –10 to 10 V: F448 to 0BB8 hex Full scale for other ranges: 0000 to 1770 hex		16-bit binary (4-digit hexadecimal) Full scale for –10 to 10 V: E890 to 1770 hex Full scale for other ranges: 0000 to 2EE0 hex			
Averaging function		Supported (Set in output words n+1 and n+2.)					
Open-circuit detection function		Supported					
Conversion time		2 ms/point (8 ms/all points)		1 ms/point (4 ms/all points)	1 ms/point (4 ms/all points)		
Isolation method		Photocoupler isolation between analog input terminals and internal circuits. No isolation between analog I/O signals.					
Current consumption		5 VDC: 100 mA max.; 24 VI	DC: 90 mA max.	5 VDC: 100 mA max.; 24 VI	DC: 50 mA max.		

Analog Output Units

Model		CP1W-DA021	/CP1W-DA041	CP1\	W-DA042
Item		Voltage output	Current output	Voltage output	Current output
Number of analog outputs		CP1W-DA021: 2 outputs (2 CP1W-DA041: 4 outputs (4		4 outputs (4 words allocate	d)
Output signal range		1 to 5 V, 0 to 10 V, or -10 to 10 V	0 to 20 mA or 4 to 20 mA	1 to 5 V, 0 to 10 V, or -10 to 10 V	0 to 20 mA or 4 to 20 mA
External output allowable	load resistance	$2 \text{ k}\Omega$ min.	350 $Ω$ max.	$2 \text{ k}\Omega$ min.	350 $Ω$ max.
External output impedance	е	0.5 Ω max.		0.5 Ω max.	
Resolution		1/6000 (full scale)		1/12000 (full scale)	
	25°C	0.4% full scale		0.3% full scale	
Overall accuracy	0 to 55°C	0.8% full scale		0.7% full scale	
	-20 to 0°C	1% full scale		0.9% full scale	
D/A conversion data		16-bit binary (4-digit hexadecimal) Full scale for –10 to 10 V: F448 to 0BB8 hex Full scale for other ranges: 0000 to 1770 hex		16-bit binary (4-digit hexadecimal) Full scale for –10 to 10 V: E890 to 1770 hex Full scale for other ranges: 0000 to 2EE0 hex	
Conversion time		CP1W-DA021: 2 ms/point (4 ms/all points) CP1W-DA041: 2 ms/point (8 ms/all points)		1 ms/point (4 ms/all points)	
Isolation method		Photocoupler isolation between analog output terminals and internal circuits. No isolation between analog I/O signals.			
Current consumption			A max.; 24 VDC: 95 mA max. A max.; 24 VDC: 124 mA max.		

Analog I/O Units

Model			CP1W-MAD42	/CP1W-MAD44	CP1W-	-MAD11	
Item			Voltage I/O	Current I/O	Voltage I/O	Current I/O	
	Number of inp	uts	4 inputs (4 words allocated)		2 inputs (2 words allocated)		
	Input signal range		0 to 5 V, 1 to 5 V, 0 to 10 V, or –10 to 10 V	0 to 20 mA or 4 to 20 mA	0 to 5 V, 1 to 5 V, 0 to 10 V, or –10 to 10 V	0 to 20 mA or 4 to 20 mA	
	Max. rated inpu	ut	±15 V	±30 mA	±15 V	±30 mA	
	External input	impedance	1 MΩ min.	Approx. 250 Ω	1 MΩ min.	Approx. 250 Ω	
	Resolution		1/12000 (full scale)		1/6000 (full scale)		
Analog Input	_	25°C	0.2% full scale	0.3% full scale	0.3% full scale	0.4% full scale	
Section	Overall accuracy	0 to 55°C	0.5% full scale	0.7% full scale	0.6% full scale	0.8% full scale	
	accuracy	-20 to 0°C	0.7% full scale	0.9% full scale	0.8% full scale	1% full scale	
	A/D conversion	n data	16-bit binary (4-digit hexadecimal) Full scale for –10 to 10 V: E890 to 1770 hex Full scale for other ranges: 0000 to 2EE0 hex		16-bit binary (4-digit hexade Full scale for –10 to 10 V: F Full scale for other ranges:	448 to 0BB8 hex	
	Averaging function		Supported		Supported (Settable for individual inputs via DIP switch		
	Open-circuit detection function		Supported				
	Number of outputs		CP1W-MAD42: 2 outputs (2 words allocated) CP1W-MAD44: 4 outputs (4 words allocated)		1 output (1 word allocated)		
	Output signal range		1 to 5 V, 0 to 10 V, or -10 to 10 V	0 to 20 mA or 4 to 20 mA	1 to 5 V, 0 to 10 V, or -10 to 10 V	0 to 20 mA or 4 to 20 mA	
	Allowable external output load resistance		2 kΩ min.	350 Ω max.	1 kΩ min.	600 Ω max.	
Analog Output	External output impedance		0.5 Ω max.		0.5 Ω max.		
Section	Resolution		1/12000 (full scale)		1/6000 (full scale)		
		25°C	0.3% full scale		0.4% full scale		
	Overall accuracy	0 to 55°C	0.7% full scale		0.8% full scale		
	accuracy	-20 to 0°C	0.9% full scale		1% full scale		
	Set data (D/A o	onversion)	16-bit binary (4-digit hexadecimal) Full scale for –10 to 10 V: E890 to 1770 hex Full scale for other ranges: 0000 to 2EE0 hex		16-bit binary (4-digit hexadecimal) Full scale for –10 to 10 V: F448 to 0BB8 hex Full scale for other ranges: 0000 to 1770 hex		
Conversion time		CP1W-MAD42: 1 ms/point (6 ms/all points) CP1W-MAD44: 1 ms/point (8 ms/all points)		2 ms/point (6 ms/all points)			
Isolation metho	d		Photocoupler isolation between analog I/O terminals and No isolation between analog I/O signals.		I internal circuits.		
Current consumption		CP1W-MAD42: 5 VDC: 120 mA max., 24 VDC: 120 mA max. CP1W-MAD44: 5 VDC: 120 mA max., 24 VDC: 170 mA max.		5 VDC: 83 mA max., 24 VDC: 110 mA max.			

Temperature Sensors Units

Item CP1W-TS001 CP1W		CP1W-TS002	CP1W-TS101	CP1W-TS102		
		Thermocouples		Platinum resistance thermome	ter	
Temperature sensors		Switchable between K and J, but same type must be used for all inputs.		Switchable between Pt100 and JPt100, but same type must bused for all inputs.		
Number of inputs		2	4	2	4	
Allocated input words		2	4	2	4	
	25°C	(The larger of ±0.5% of converte	ed value or ±2°C) ±1 digit max.	(The larger of ±0.5% of converted value or ±1°C) ±1 digit max.		
Accuracy	0 to 60°C	(The larger of ±1% of converted	l value or ±4°C) ±1 digit max.	(The larger of ±1% of converted value or ±2°C) ±1 digit max.		
	-20 to 0°C	(The larger of ±1.3% of converte	d value or ±5°C) ±1 digit max. * 1	(The larger of ±1.3% of converted value or ±3°C) ±1 digit max.		
Conversion tir	ne	250 ms for 2 or 4 input points				
Converted temperature data		16-bit binary data (4-digit hexadecimal)				
Isolation		Photocouplers between all temp	perature input signals			
Current consumption		5 VDC: 40 mA max., 24 VDC: 5	9 mA max.	5 VDC: 54 mA max., 24 VDC: 73 mA max.		

^{*1.} Accuracy for a K-type sensor at -100°C or less is ±4°C ±1 digit max.

The rotary switch is used to set the temperature range.

Cot	tina	CP1W-TS001/TS002			CP1W-TS101/TS102		
Set	ting	Input type	Range (°C)	Range (°F)	Input type	Range (°C)	Range (°F)
F O :	0	V	-200 to 1,300	-300 to 2,300	Pt100	-200.0 to 650.0	-300.0 to 1,200.0
	1	N.	0.0 to 500.0	0.0 to 900.0	JPt100	-200.0 to 650.0	-300.0 to 1,200.0
	2	1	-100 to 850	-100 to 1,500			
681	3	J	0.0 to 400.0	0.0 to 750.0		Cannot be set.	
	4 to F		Cannot be set.				

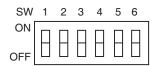
Main Specifications

Item			CP1W-TS003
Tamanaratura			Thermocouples or analog input *1
Temperature s	sensors		Switchable between K and J, but same type must be used for all inputs.
Number of inp	Number of inputs		4
Allocated inpu	Allocated input words		4
Max. number	of Units		3
		Thermocouple inputs	(The larger of ±0.5% of converted value or ±2°C) ±1 digit max. *2
	25°C	Analog voltage inputs	0.5% full scale
		Analog current inputs	0.6% full scale
		Thermocouple inputs	(The larger of ±1% of converted value or ±4°C) ±1 digit max. *3
Accuracy	0 to 60 °C	Analog voltage inputs	1.0% full scale
		Analog current inputs	1.2% full scale
		Thermocouple inputs	(The larger of ±1.3% of converted value or ±5°C) ±1 digit max. *3
	-20 to 0 °C	Analog voltage inputs	1.2% full scale
		Analog current inputs	1.5% full scale
		Thermocouple inputs	K: -200.0 to 1300.0°C or -300.0 to 2300.0°F J: -100.0 to 850.0°C or -100.0 to 1500.0°F
Input signal ra	inge	Analog voltage inputs	0 to 10V/1 to 5V
		Analog current inputs	4 to 20mA
Resolution		Thermocouple inputs	0.1°C or 0.1°F
Resolution		Analog inputs	1/12000 (full scale)
Max. rated inp	+	Analog voltage inputs	±15V
wax. rateu iiip	ut	Analog current inputs	±30mA
External input	impedance	Analog voltage inputs	1 Μ Ω min.
External input	impedance	Analog current inputs	250Ω
Open-circuit o	letection function	1	Supported
Averaging fun	ction		Unsupported
Conversion ti	me		250 ms for 4 input points
Converted ten	nperature data		16-bit binary data (4-digit hexadecimal) 2-decimal-place mode is not supported
Converted AD	data		16-bit binary data (4-digit hexadecimal)
Isolation			Photocouplers between any two input signals
Current consu	ımption		5 VDC: 70 mA max., 24 VDC: 30 mA max.

- ***1.** Only last two channels can be used as analog input. ***2.** Accuracy for a K-type sensor at -100°C or less is ±4°C ±1 digit max.
- ***3.** Accuracy for a K-type sensor at -100°C or less is ±10°C ±1 digit max.

DIP Switch Settings

With the Temperature Sensor Unit's DIP switch, set the input type (temperature or analog input), the input thermocouple type (K or J) and the temperature unit (°C or °F).



SW	Setting					
1	Thermosouple type of temperature concer	ON	J			
ı	Thermocouple type of temperature sensor	OFF	K			
2	Tammaratura unit	ON	°F			
2	Temperature unit	OFF	°C			
3	NC		·			
4	Input type selection for the third input (Input 2)	ON	Analog input			
4	input type selection for the third input (input 2)	OFF	Thermocouple			
5	Input time colories for the fourth input (Input 2)	ON	Analog input			
5	Input type selection for the fourth input (Input 3)	OFF	Thermocouple			
6	Analog input signal range	ON	1 to 5V/4 to 20mA			
6	Analog input signal range	OFF	0 to 10V			

	Temperature input	Analo	og input	
Input type	Range (°C)	Range (°F)	Input type	Range
K	-200.0 to 1300.0	-300.0 to 2300.0	Voltage	0 to 10V/1 to 5V
J	-100.0 to 850.0	-100.0 to 1500.0	Current	4 to 20mA

Main Specifications

Item		CP1W-TS004
Townsystems come		Thermocouples
Temperature sense	ors	Switchable between K and J, but same type must be used for all inputs.
Number of inputs		12
Allocated input wo	rds	2
Allocated output w	cated output words 1	
	25°C	(The larger of ±0.5% of converted value or ±2°C) ±1 digit max. * 1
Accuracy	0 to 60°C	(The larger of ±1% of converted value or ±4°C) ±1 digit max. *2
	-20 to 0°C	(The larger of ±1.3% of converted value or ±5°C) ±1 digit max. *2
Conversion time		500 ms for 12 input points
Converted temperature data		16-bit binary data (4-digit hexadecimal) 2-decimal-place mode is not supported
Isolation		Photocouplers between any two input signals
Current consumpti	on	5 VDC: 80 mA max., 24 VDC: 50 mA max.

DIP Switch Settings

With the Temperature Sensor Unit's DIP switch, set the temperature unit and the temperature input range.



sw	Setting				
4	Innut tune	ON	J		
1	Input type	OFF	K		
2	Temperature unit	ON	°F		
2		OFF	°C		

Temperature input				
Input type	Range (°C)	Range (°F)		
K	-200.0 to 1300.0	-300.0 to 2300.0		
J	-100.0 to 850.0	-100.0 to 1500.0		

^{*1} Accuracy for a K-type sensor at -100°C or less is ±4°C ±1 digit max. *2 Accuracy for a K-type sensor at -100°C or less is ±10°C ±1 digit max.

Comparison of specifications of CP2E and CP1E

• 151 11		CP2E			CF	P1E	
Specifications	CP2E-N□□	CP2E-S□□	CP2E-E□□	CP1E-N□□	CP1E-N□□S□	CP1E-E□□S	CP1E-NA20
Number of built-in I/O points	14/20/30/40/60	30/40/60	14/20/30/40/60	14/20/30/40/60	30/40/60	10/14/20/30/40/60	20
Total number of Expansion units	14/20 points None 30/40/60 points 3 u	ınits	1	14/20 points None 30/40/60 points 3 units			
Lineup Output/power supply type	Relay/AC Relay/DC Transistor (sinking)/AC Transistor (sinking)/DC Transistor (sourcing)/DC	Relay/AC Transistor (sinking)/DC Transistor (sourcing)/DC	• Relay/AC	Relay/AC Relay/DC Transistor (sinking)/AC Transistor (sourcing)/AC Transistor (sinking)/DC Transistor (sourcing)/DC	Relay/AC Transistor (sinking)/DC Transistor (sourcing)/DC	Relay/AC Only 10 points Relay/DC Transistor (sinking)/AC Transistor (sourcing)/AC Transistor (sinking)/DC Transistor (sinking)/DC Transistor (sourcing)/DC	Relay/AC Transistor (sinking)/DC Transistor (sourcing)/DC
Program capacity	10K steps	8K steps	4K steps	8K steps	8K steps	2K steps	8K steps
FB capacity	10K steps	8K steps	4K steps	None			
Function block steps	Yes Languages usable Ladder diagrams, s	in function block destructured text (ST)	finitions:	None			
Overhead processing time	0.2 ms	0.15 ms	0.1 ms	0.4 ms			
Instruction execution times	LD 0.23 μs MOV 1.76 μs			LD 1.19 μs MOV 7.9 μs		T.	
Data memory capacity	16K words	8K words	4K words	8K words	8K words	2K words	8K words
IO Memory backup	Built-in non-volatile	memory (Batteryles	ss backup)	Built-in SRAM (Ba	ttery backup)		
Pulse outputs	N14/20: 2 outputs 100 kHz N30/40/60: 4 outputs 100 kHz (Linear interpolation possible)	2 outputs 100 kHz	None	2 outputs 100kHz	2 outputs 100kHz	None	2 outputs 100kHz
High-speed counters (Differential Phase)	N14/20: 2 counters (50 kHz, 5 kHz) N30/40/60: 2 counters (50 kHz x2)	2 counters (50 kHz, 5 kHz)		2 counters (50 kHz	z,5 kHz)	2 counters (5 kHz x2)	2 counters (50 kHz, 5 kHz)
Quick-response Interrupt inputs	8 inputs (6 inputs only for 14 points)	6 inputs		6 inputs (4 inputs only for 1	0 points)		
Ethernet port	Included N14/20: 1 port N30/40/60: 2 port	None	None	None N30/40/60 only: 1 port (CP1W-CIF41 use)	None	None	1 port (CP1W-CIF41 use)
USB port	None	Included	Included	Included	+		1
Serial port	N14/20: Max 2 port (Option boards use) N30/40/60: Max 3 port (Option boards use)	2 port RS-232C RS-485	1 port RS-232C	N14/20: 1 port RS-232C N30/40/60: Max 2 port RS-232C Option board	1 port RS-232C N30/40/60 S1 Type only: Max 2 port RS-232C RS-485	None	Max 2 port RS-232C Option board
Serial communication protocols	Host Link 1: N NT Link (1: N) No-protocol mode Serial PLC Link (master, slave) Modbus-RTU easy master Modbus-RTU Slave			Host Link 1: N NT Link (1: N') No-protocol mode Serial PLC Link (m Modbus-RTU easy	naster, slave)		
Option Boards	N14/20: 1 unit N30/40/60: 2 units	None	None	N14/20: None N30/40/60: 1 unit	None	None	1 unit
Clock function	Yes	Yes	None	Yes	Yes	None	Yes
Corresponding battery	CP2W-BAT02 (for clock function)		None	CP1W-BAT01 (for clock function,	IO memory backup)	None	CP1W-BAT01
Built-in analog	None		1	None	- 17		Analog input 2channels Analog output 1channel
Analog adjusters	None	None	None	Yes	None	E□□: Yes E□□S: None	Yes

Easy to convert CP1E code into CP2E code

Uploaded CP1E code can be converted into CP2E code with just one click.



Cx-One Ver 4.51 or higher Cp-programmer Ver 9.72 or higher

Examples of replacement of CP1E with CP2E

CP1E-E□□/E□□S	CP2E-E□□
CP1E-E10D□-□	CP2E-E14DR-A or others
CP1E-E14SDR-A/E14DR-A	CP2E-E14DR-A
CP1E-E20SDR-A/E20DR-A	CP2E-E20DR-A
CP1E-E30SDR-A/E30DR-A	CP2E-E30DR-A
CP1E-E40SDR-A/E40DR-A	CP2E-E40DR-A
CP1E-E60SDR-A	CP2E-E60DR-A

CP1E-N□□S	CP2E-S□□
CP1E-N30SD□-□/N30S1D□-□	CP2E-S30D□-□
CP1E-N40SD□-□/N40S1D□-□	CP2E-S40D□-□
CP1E-N60SD□-□/N60S1D□-□	CP2E-S60D□-□

CP1E-N□□	CP2E-S□□ * 1	CP2E-N□□ * 1	
CP1E-N14D□-□	-	CP2E-N14D□-□	
CP1E-N20D□-□	-	CP2E-N20D□-□	
CP1E-N30D□-□	CP2E-S30D□-□	CP2E-N30D□-□	
CP1E-N40D□-□	CP2E-S40D□-□	CP2E-N40D□-□	
CP1E-N60D□-□	CP2E-S60D□-□	CP2E-N60D□-□	
CP1E-NA20□-□	-	CP2E-N30D□-□ + CP1W-MAB221 or others	

^{*1.} When the AC powered N30/40/60 CPU Unit with relay outputs, or the DC powered N30/40/60 CPU Unit with transistor outputs is used without an option board or with the CP1W-CIF11 Option Board, it is recommended to replace with the CP2E-S□□. When any of the other CPU units is used with an option board, it is recommended to replace with the CP2E-N□□. For details, refer to the *Replacement Guide from CP1E to CP2E* (Cat. No. P150).

CP2E

Related Manuals

Manual name	Cat. No.	Model numbers	Application	Contents
SYSMAC CP Series CP2E CPU Unit Hardware User's Manual	W613	CP2E-E DD - CP2E-N	To learn the hardware specifications of the CP2E PLCs	Describes the following information for CP2E PLCs. Overview and features Basic system configuration Part names and functions Installation and settings Troubleshooting
SYSMAC CP Series CP2E CPU Unit Software User's Manual	W614	CP2E-E D D - CP2E-S D D - CP2E-N D - CP2E-N D - CP2E-N D D D - CP2E-N	To learn the software specifications of the CP2E PLCs	Describes the following information for CP2E PLCs. CPU Unit operation Internal memory Programming Settings CPU Unit built-in functions Interrupts High-speed counter inputs Pulse outputs Serial communications Ethernet Other functions
SYSMAC CP Series CP1E/CP2E CPU Unit Instructions Reference Manual	W483	CP1E-E DD - CP1E-N DD - CP1E-N DD - CP1E-N C	To learn programming instructions in detail	Describes each programming instruction in detail.
CS/CJ/CP/NSJ Series Communications Commands Reference Manual	W342	CS1G/H-CPU	To learn communications commands for CS/CJ/CP/NSJ-series Controllers in detail	Describes 1) C-mode commands and 2) FINS commands in detail. Read this manual for details on C-mode and FINS commands addressed to CPU Units.
CX-One FA Integrated Tool Package Setup Manual	W463		To install the software provided in the CX-One	Describes the overview of the CX-One FA Integrated Tool Package, and how to install and uninstall the CX-One.
CX-Programmer Operation Manual	W446		To learn the operation procedures for the CX-Programmer, the Programming Device for Windows computers	Describes the operation procedures for the CX-Programmer.
CX-Programmer Operation Manual (Function Blocks/ Structured Text)	W447	CXONE-AL□□D-V4		
CX-Simulator Operation Manual	W366		To learn the operation procedures for the CX-Simulator, the Simulation Device for Windows computers	Describes the operation procedures for the CX-Simulator.
CX-Integrator Operation Manual	W464		To set up and monitor networks	Describes the operation procedures for the CX-Integrator.

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